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UART vs SPI vs I2C | Difference between UART,SPI and I2C

Zorlu koşullarda servis avantajları yanınızda.

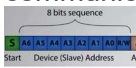


This page compares UART vs SPI vs I2C interfaces and mentions difference between UART, SPI and I2C in tabular format. It provides comparison between these interfaces based on various factors which include interface diagram, pin designations, data rate, distance, communication type, clock, hardware and software complexity, advantages, disadvantages etc.

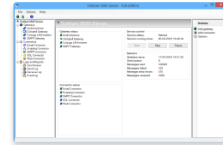
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Features	UART	SPI	I2C
Full Form	Universal Asynchronous Receiver/Transmitter	Serial Peripheral Interface	Inter-Integrat
Interface Diagram	<p style="text-align: center;">UART Interface Diagram</p>	<p style="text-align: center;">SPI Interface Diagram</p>	<p style="text-align: center;">I2C Int</p>
Pin Designations	TxD: Transmit Data Rx D: Receive Data	SCLK: Serial Clock	SDA: Serial SCL: Serial

		MOSI: Master Output, Slave Input MISO: Master Input, Slave Output SS: Slave Select	
Data rate	As this is asynchronous communication, data rate between two devices wanting to communicate should be set to equal value. Maximum data rate supported is about 230 Kbps to 460kbps.	Maximum data rate limit is not specified in SPI interface. Usually supports about 10 Mbps to 20 Mbps	I2C supports 100 kbps, 3.4 Mbps also supports 10 Mbps.
Distance	Lower about 50 feet	highest	Higher
Type of communication	Asynchronous	Synchronous	Synchronous
Number of masters	Not Application	One	One or more
Clock	No Common Clock signal is used. Both the devices will use their independent clocks.	There is one common serial clock signal between master and slave devices.	There is a common clock between multiple slaves.
Hardware complexity	lesser	less	more
Protocol	For 8 bits of data one start bit and one stop bit is used.	Each company or manufacturers have got their own specific protocols to communicate with peripherals. Hence one needs to read datasheet to know read/write protocol for SPI communication to be established. For example we would like SPI communication between microcontroller and EPROM. Here one needs to go through read/write operational diagram in the EPROM data sheet.	It uses start and stop data which data has bit rate. Figure depicts communication sequence. 
Software	As this is one to one	Slave select	There will be

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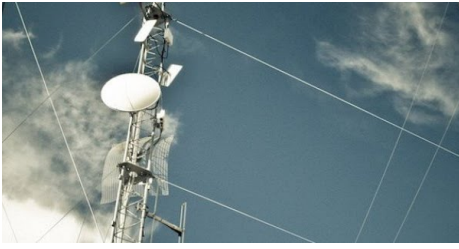
Software addressing	As this is one to one connection between two devices, addressing is not needed.	Slave select lines are used to address any particular slave connected with the master. There will be 'n' slave select lines on master device for 'n' slaves.	and multip masters ca all the slav devices ca connected. I2C interfa
Advantages	<ul style="list-style-type: none"> • It is simple communication and most popular which is available due to UART support in almost all the devices with 9 pin connector. It is also referred as RS232 interface. 	<ul style="list-style-type: none"> •It is simple protocol and hence so not require processing overheads. •Supports full duplex communication. •Due to separate use of CS lines, same kind of multiple chips can be used in the circuit design. •SPI uses push-pull and hence higher data rates and longer ranges are possible. •SPI uses less power compare to I2C 	<ul style="list-style-type: none"> •Due to op limited slew achieved. •More than be used in design. •Needs fe for commu •I2C addr which doe lines used to add extr bus. •It uses op concept. H voltage fle; interface b •Uses flow
Disadvantages	<ul style="list-style-type: none"> • They are suitable for communication between only two devices. • It supports fixed data rate agreed upon between devices initially before communication otherwise data will be garbled. 	<ul style="list-style-type: none"> • As number of slave increases, number of CS lines increases, this results in hardware complexity as number of pins required will increase. • To add a device in SPI requires one to add extra CS line and changes in software for particular device addressing is concerned. •Master and slave relationship can not be changed as usually done in I2C interface. •No flow control available in SPI. 	<ul style="list-style-type: none"> •Increases circuit whe and maste •I2C interf •Requires control the it needs sc overheads microcontr
Reference	RS232 Interface>>	SPI Interface>>	I2C Interfa

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I2C UART

I2C SPI

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